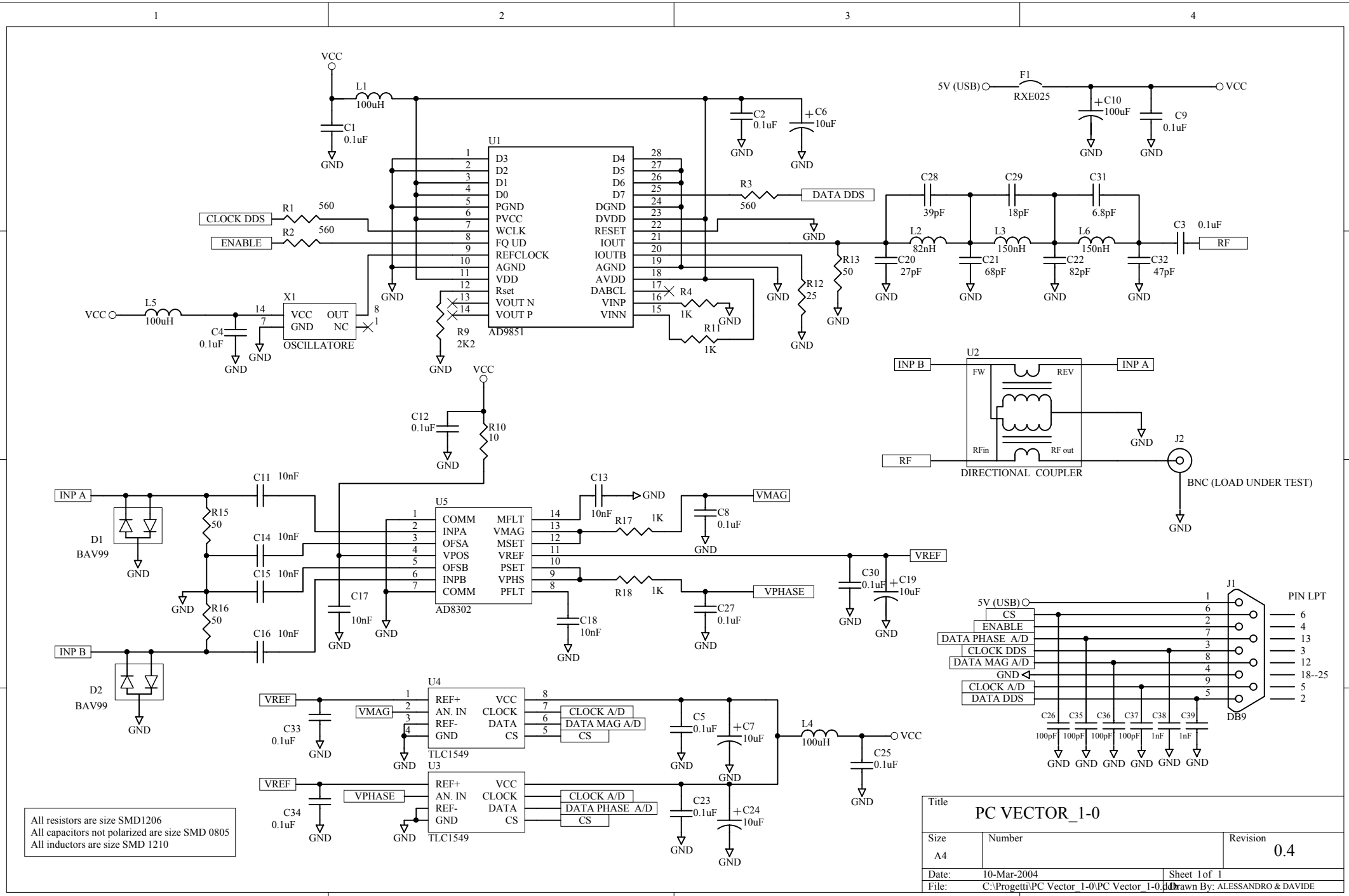
VCC
L1
100uH
C1
0.1uF
GND
U1
D3
D2
D1
D0
PGND
PVCC
WCLK
FQ UD
REFCLOCK
AGND
VDD
Rset
VOUT N
VOUT P
D4
D5
D6
D7
DGND
DVDD
RESET
IOUT
IOUTB
AGND
AVDD
DABCL
VINP
VINN
AD9851
CLOCK DDS
ENABLE
R1
560
R2
560
R3
560
DATA DDS
C2
0.1uF
C6
10uF
R4
1K
R11
1K
R12
25
R13
50
R9
2K2
X1
VCC
GND
OUT
NC
OSCILLATORE
L5
100uH
C4
0.1uF
5V (USB)
F1
RXE025
C10
100uF
C9
0.1uF
C28
39pF
C29
18pF
C31
6.8pF
L2
82nH
L3
150nH
L6
150nH
C20
27pF
C21
68pF
C22
82pF
C32
47pF
C3
0.1uF
RF
U2
INP B
INP A
FW
REV
RFin
RF out
DIRECTIONAL COUPLER
J2
BNC (LOAD UNDER TEST)
C12
0.1uF
R10
10
C11
10nF
C14
10nF
C15
10nF
C16
10nF
R15
50
R16
50
D1
BAV99
D2
BAV99
U5
COMM
INPA
OFSA
VPOS
OFSB
INPB
COMM
MFLT
VMAG
MSET
VREF
PSET
VPHS
PFLT
AD8302
C13
10nF
R17
1K
C8
0.1uF
VMAG
VREF
C30
0.1uF
C19
10uF
R18
1K
VPHASE
C27
0.1uF
C17
10nF
C18
10nF
U4
REF+
AN. IN
REF-
GND
VCC
CLOCK
DATA
CS
TLC1549
CLOCK A/D
DATA MAG A/D
CS
C33
0.1uF
C5
0.1uF
C7
10uF
L4
100uH
C25
0.1uF
U3
VPHASE
DATA PHASE A/D
C34
0.1uF
C23
0.1uF
C24
10uF
J1
PIN LPT
DB9
CS
ENABLE
DATA PHASE A/D
CLOCK DDS
DATA MAG A/D
CLOCK A/D
DATA DDS
C26
C35
C36
C37
C38
C39
100pF
1nF
All resistors are size SMD1206
All capacitors not polarized are size SMD 0805
All inductors are size SMD 1210
Title
PC VECTOR_1-0
Size
A4
Number
Revision
0.4
Date:
10-Mar-2004
Sheet 1of 1
File:
C:\Progetti\PC Vector_1-0\PC Vector_1-0.dd
Drawn By: ALESSANDRO & DAVIDE